

*Free  
PCB Design Courses  
Don't Delay!*



6625 Flinders Drive  
San Diego, California 92121-3786

# Advanced PCB Design

*Free Seminar  
From ACCEL  
Technologies*



*Do join us,  
won't you?*



*Hey, are you going to PCB 'N, too?*

*Nopes. I can't make it, but I am gonna get me some free email software!*

*So what're you gonna do after graduation?*

*Why, continue my studies of course... at PCB 'N.*

*Wow, let's all pile into dad's Edsel and go today!*

PLACE  
ADDRESS  
HERE



PCB Technologies, Inc.

4821 Haddon Drive

San Diego, California 92121-2994

# You are invited...

... to revisit your college days with a trip to PCB U. Pursue high honors in design productivity at our free EDA mini-seminar. The course schedule includes sessions on handling mixed signal processing, preparing board designs for manufacturing, and optimizing ACCEL EDA's interoperability with other best-of-class EDA design tools.



*... come! What a small record! Can't wait to see it. (Actually it's software! For you! From me!)*

## Major in PCB design.

Bring your bluebook and your number two pencil and you'll learn more in a day at PCB U. than in a month of independent study. Using the Windows-based ACCEL EDA suite of tools as our design environment, we'll cram your head full of knowledge guaranteed to streamline your design process from schematic entry to PCB layout to manufacturing. Discover the power of schematic entry and simulation tools from Viewlogic, placement and autorouting tools from Cooper and Chyan, and manufacturing output tools from



*Your distinguished professor will conduct a lively course with industry-leading ACCEL EDA for Windows.*

Advanced CAM Technology. Explore the unique flexibility of DBX,

ACCEL's application programming interface. And—one

lucky scholar in each city will win a \$1000.00

grant towards purchase of ACCEL EDA.

You don't have to save up for tuition (it's free!) but class space is limited.

Call, fax, or email today to enroll!

*Study with a buddy!  
Bring a friend or  
the boss along.*

*Go  
can't wait  
my name*

Limited Seating Register Today!

## Registration Instructions:

Call 800 488-0680

Call in your registration - we'll provide the location and time of your seminar.

Or Fax 619 554-1019

Copy this page, circle the date and city, fill in the bubble next to the course numbers you will be attending, and complete the card below. You may choose any or all sessions. Be sure to double check the location listing for the third party course offered in your city. For the completed page back to us. We'll call you with the location and time of your seminar.

Or Email [pcba@accelltech.com](mailto:pcba@accelltech.com)  
Email us your date, city, and course choices.  
Don't forget to include your phone number.

## Fall '96 Seminar Series:

9/9	Mon.	Salt Lake City, UT
9/10	Tues.	Piano, TX, Boulder, CO and Gaithersburg MD
9/11	Wed.	Austin, TX, South Denver, CO and Hampton, VA
9/12	Thurs.	Houston, TX, Santa Clara, CA and RTP, NC
9/13	Fri.	Tempe, AZ and Pleasanton, CA
9/16	Mon.	Overland Park, MO
9/17	Tues.	Mt. Laurel, NJ and Pittsburgh, PA
9/18	Wed.	Whippany, NJ and Norvi, MI
9/19	Thurs.	Smithtown, NY and Rolling Meadows, IL
9/20	Fri.	Singhington, NY and Arden Hills, MN
9/24	Tues.	Meriden, CT
9/25	Wed.	Saxborough, MA
9/26	Thurs.	Rochester, NY
10/1	Tues.	Norcross, GA
10/2	Wed.	Orlando, FL
10/3	Thurs.	Huntsville, AL
10/8	Tues.	El Segundo, CA
10/9	Wed.	Irvine, CA
10/10	Thurs.	San Diego, CA
10/15	Tues.	Richmond, Canada
10/16	Wed.	Bellevue, WA
10/18	Thurs.	Seas, ID
10/22	Tues.	Montreal, Canada
10/23	Wed.	Ottawa, Canada
10/24	Thurs.	Toronto, Canada

- EDA 101
  - EDA 451
  - EDA 256
  - EDA 367
  - EDA 479
- Call me to set up a demo!  
The elective I want to attend is not offered in my city.

The course on Viewlogic (EDA 479c) will be given at other points in time.  
The course on OCT placement tools and routers (EDA 479f) will be given at other points in time.

Don't Delay!

## Table of Classes

Location and Registration 8:30 a.m.

### NOTES

**101 Introduction to PCB Design**  
**8:45-9:45 a.m.**  
An integrated approach for today's most critical designs. Emphasis on EDA's "correct by design" philosophy and auto methodology. Schematic capture, placement, board design, and generation bar and other manufacturing data.

**A 451 Advanced Topics: Design for Mixed Signal Processing**  
**Ann A. Lopez 10:00-11:00 a.m.**  
Use powerful features to handle complex signal designs. Learn how to create splits with automatic thermal ties and accurate checking. Create positive copper pours, island removal and trace plating, use job-specific rules to establish backoffs.

**EDA 256 Easy Prep for Manufacturing**  
**Ed. Bob Beem 11:15 a.m.-12:15 p.m.**  
Use how ACCEL EDA eases the manufacturing stress by generating fabrication and assembly drawings, customized bills of materials, and drill settings. View board panelization techniques. See specialized utilities for test point reporting, and Windows shortcuts for complete documentation.

○ EDA 367  
**Collegium on Interoperability**  
**Prof. Lisa M. Ahl**

**1:30-2:30 p.m.**  
Interface with other best-of-class EDA design tools through ACCEL TIES program (Tightly Integrated Engineering Solutions). Viewlogic for schematic entry and simulation, CCT for shape-based autorouters, Advanced CAM Technologies for back-end manufacturing tools. Discover DBL, ACCEL's application programming interface to create programs that interact live with Schematic and PCB databases.

### Elective

○ EDA 479  
**Seminar: Third Party Topics 2:45-3:45**  
\*\*Check location listing for class in your city.

○ EDA 479a  
**Advanced Design Entry: Viewlogic**  
**Prof. Don Ziser**  
Learn how Viewlogic's high-performance design tools minimize product development time and expense. ViewDraw design entry, ViewSim simulation, and ViewDatabook show design cycles from your product development process. Linked with ACCEL EDA via ACCEL TIES/VIEWDRAW.

-or-

○ EDA 479b  
**Advanced Place and Route: Cooper and Chyen Technology**  
**Prof. Otto van Beest**  
See how CCT's automatic placement and shape-based autorouting tools increase design performance and reduce manufacturing costs. Take a close look at the SPECCEIRA Autoplacement and Autorouter with advanced options to place parts and route for best design performance while following design-specific rules.

Dig these hot new tools, Sandra!

Can't make it to class? Complete this card for a FREE full-function evaluation software package or to set up a personal presentation.

Name \_\_\_\_\_

Company \_\_\_\_\_

Address \_\_\_\_\_

City \_\_\_\_\_

State \_\_\_\_\_

Zip \_\_\_\_\_

